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A circult assembly is frequency used in many electrical apparatus or devices of almost all aspects, typical application examples of which may include display devices inclusive of a liquid crystal device, a plasma device, DMD, and a electrochromic device; image sensors, inclusive of a thin-film type sensor comprising amorphous silicon, and a multi-tip-type sensor provided with an arranged plurality of IC chips; recording heads, inclusive of a thermal head and an ink jet head; and light-emitting device arrays, inclusive of an LED array, and an electron discharge device array.

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As shown in FIG. 3, the camera 10 is disposed on a backside of the liquid crystal panel P (opposite side of the liquid crystal-drive TAB film 7), so that the substrate-side mark is observed through the glass substrate 3 and the TAB-side mark is observed through the glass substrate 3 and transparent films (transparent conductor films and the anisotropic conductive adhesive, etc.) formed on the substrate.

United States Patent (187) Takahashi et al.

[11]	Patent Numbers	5,729,31.
[43]	Date of Patents	Mar. 17, 199

[54]	CIRCUIT ABSENDEN AND PROCESS FOR PRODUCTION TREASURY
De.	Property Manual School Columbs

PRODUCTIO	COL ESTERON	DATEMEN	2/1987	Brangess Pal.
ת ת	ksamni Tukuhuhi, Crigonish 1822 Salio, Dehrus Eldeo Muris 1882 Salio, Dehrus Eldeo Muris 1883 Salionis, Crigonis, ali of Japan	60-(17325 0-(18860)	9/1965 3/1992	Jugui

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12.23	U.S. Cl
(HE)	749/132 Field of Search
	359/42, 1/9, 207, 152
βØ	References Clark
	U.S. PATENT DOCUMENTS

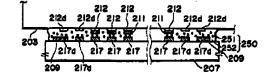
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[37] ARSTRACT
A circult assentity spinits for constituting, e.g., a liquid on yald displayed judies, is constituting to incorporate first substituts bering thereon a plurality of first electrodes and a secret softition bering thereon a spirality of more described by a second destroyed and a family expensed with the first valents on that the partially expensed with the first valents on that the partially example of the first section of that the partially example of the second abstracts are superposed and distributly constitute layer each other. The first storage has a light-enaminative layer and an oppose layer landacid with the light terminative.

each othe. The first sterrode has a light-manufative layer and a copies have bright said with the light innominate layer, and a portion of the first electrode superposed with the second contextual at least partially factories a light-manufative portion. The first or somet motories has a light-manufative portion for the first descripts. The first statement mark at a position throwed corresponding to the light-terminative portion of the first descripts. The first induction is the casteries for contributed with another first induction in the casteries contributed with another first induction in the casteries for contributed the school of first interest to be anotherful at light drysmid thereberrown to first a bright for suit all down. The second submanufacture are contributed to the little state of the first stream.

48 Claims, 13 Drawing Streets



L Number	Hits	Search Text	DB	Time stamp
1	428	foil with plastic with glass with metal	USPAT;	2002/07/12
_		1	US-PGPUB	16:24
2	341731	semiconductor or "integrated circuit"	USPAT;	2002/07/12 16:19
	100	(fail with plactic with glass with matal) and (comican dustan an	US-PGPUB	0000/07/10
3	100	(foil with plastic with glass with metal) and (semiconductor or "integrated circuit")	USPAT; US-PGPUB	2002/07/12 16:20
4	95	(foil with plastic with glass with metal) with substrate	USPAT;	2002/07/12
4	93	(1011 With plastic with glass with metal) with substrate	US-PGPUB	16:22
5	53	(semiconductor or "integrated circuit") and ((foil with plastic	USPAT;	2002/07/12
		with glass with metal) with substrate)	US-PGPUB	16:39
6	26	foil with plastic with glass with metal with rigid	USPAT;	2002/07/12
			US-PGPUB	16:24
7	11	((foil with plastic with glass with metal) with substrate) and	USPAT;	2002/07/12
_	_	(foil with plastic with glass with metal with rigid)	US-PGPUB	16:25
8	281	"anisotropic conductor" or "anisotropic adhesive"	USPAT;	2002/07/12
_			US-PGPUB	16:26
9	0	(semiconductor or "integrated circuit") and ((foil with plastic	USPAT;	2002/07/12
		with glass with metal) with substrate) and ("anisotropic	US-PGPUB	16:26
10	1.	conductor" or "anisotropic adhesive") ((foil with plastic with glass with metal) with substrate) and	TICDATE.	0000/07/10
10	11	LED	USPAT; US-PGPUB	2002/07/12
11	5590	led with transistor	USPAT;	16:30 2002/07/12
11	3390	ica with transistor	US-PGPUB	16:39
12	2	("anisotropic conductor" or "anisotropic adhesive") and (led	USPAT;	2002/07/12
	_	with transistor)	US-PGPUB	16:40
13	26	"light emmitting diode"	USPAT;	2002/07/12 16:41
			US-PGPUB	
14	2	"light emmitting diode" with transistor	USPAT;	2002/07/12 16:41
			US-PGPUB	
15	13	(led or "light emitting diode") and transistor and ("anisotropic	USPAT;	2002/07/12
		conductor" or "anisotropic adhesive")	US-PGPUB	16:58
-	98	(438/28).CCLS.	USPAT;	2002/07/12
			US-PGPUB	16:18
-	55231	stacking	USPAT;	2002/07/09
_	7060	a dimensional or "a dimensional"	US-PGPUB	14:36
-	7969	3-dimensional or "3 dimensional"	USPAT; US-PGPUB	2002/07/09
_	587	anisotropic with conductor	USPAT;	14:36 2002/07/09
	307	anisotropic with conductor	US-PGPUB	14:36
_	98	(438/28).CCLS.	USPAT;	2002/07/09
İ	, -	(10-7-5)	US-PGPUB	14:36
-	55231	stacking	USPAT;	2002/07/09
			US-PGPUB	14:36
-	7969	3-dimensional or "3 dimensional"	USPAT;	2002/07/09
	_		US-PGPUB	15:02
-	587	anisotropic with conductor	USPAT;	2002/07/09
]	. =	((+00/00) COLO) and (-01-04-04-14-14-14-14-14-14-14-14-14-14-14-14-14	US-PGPUB	14:40
-	. 0	((438/28).CCLS.) and (anisotropic with conductor)	USPAT;	2002/07/09
_	^	((438/28).CCLS.) and (3-dimensional or "3 dimensional")	US-PGPUB	14:41
·	0	((450) 20). Colo.) and (3-dimensional)	USPAT; US-PGPUB	2002/07/09
_	13	((438/28).CCLS.) and stacking	US-PGPUB USPAT;	14:41 2002/07/09
İ	. 5	(1700) 20), COLO., and Stacking	US-PGPUB	14:41
-	71399	3-dimensional or "3 dimensional" or 3D	USPAT;	2002/07/09
	7-077		US-PGPUB	15:04
-	516	438/107.ccls.	USPAT;	2002/07/09
l	-		US-PGPUB	15:08
- [5	(anisotropic with conductor) and 438/107.ccls.	USPAT;	2002/07/09
			US-PGPUB	15:14
-	299	438/109.ccls.	USPAT;	2002/07/09
		Control 1 11 1 1 2 1 24	US-PGPUB	15:14
-]	0	(anisotropic with conductor) and 438/109.ccls.	USPAT;	2002/07/09
		(a dimensional or "a dimensional") 3 0/1-	US-PGPUB	15:14
-	9	(3-dimensional or "3 dimensional") and 438/109.ccls.	USPAT;	2002/07/09
			US-PGPUB	15:14

-	6	stacking and ((3-dimensional or "3 dimensional") and	USPAT;	2002/07/09
		438/109.ccls.)	US-PGPUB	15:30
-	198	438/119.ccls.	USPAT;	2002/07/09
			US-PGPUB	15:30
-	3	(anisotropic with conductor) and 438/119.ccls.	USPAT;	2002/07/09
			US-PGPUB	15:31
-	o	stacking and (3-dimensional or "3 dimensional") and	USPAT;	2002/07/09
ļ	_	438/119.ccls.	US-PGPUB	15:31
_	439	438/455.ccls.	USPAT;	2002/07/09
	40)	400/400/0000	US-PGPUB	15:33
_	1	(anisotropic with conductor) and 438/455.ccls.	USPAT;	2002/07/09
	•	(unisotropic with conductor) and 450/455.ccis.	US-PGPUB	• •
_	1	stacking and (3-dimensional or "3 dimensional") and	USPAT;	15:34
_	1			2002/07/09
	0.00	438/455.ccls.	US-PGPUB	15:34
-	252	438/458.ccls.	USPAT;	2002/07/09
	_	(' ' ' ' ' ' ' ' ' ' ' ' ' ' ' ' ' ' '	US-PGPUB	15:34
-	0	(anisotropic with conductor) and 438/458.ccls.	USPAT;	2002/07/09
			US-PGPUB	15:34
-	1	stacking and (3-dimensional or "3 dimensional") and	USPAT;	2002/07/09
		438/458.ccls.	US-PGPUB	15:35
-	38	438/610.ccls.	USPAT;	2002/07/09
			US-PGPUB	15:35
- :	1	(anisotropic with conductor) and 438/610.ccls.	USPAT;	2002/07/09
			US-PGPUB	15:35
-	o	stacking and (3-dimensional or "3 dimensional") and	USPAT;	2002/07/09
		((anisotropic with conductor) and 438/610.ccls.)	US-PGPUB	15:35
_	2	stacking and (3-dimensional or "3 dimensional") and	USPAT;	2002/07/09
	-	(anisotropic with conductor)	US-PGPUB	15:36
-	41	438/for.426.ccls.	EPO; JPO;	2002/07/09
	7-	4,00/101.420.0000.	DERWENT	15:36
_	554	anisotropic with conductor	EPO; JPO;	2002/07/09
	55 4	amsotropic with conductor	DERWENT	
_	•	438/for.426.ccls. and (anisotropic with conductor)		15:37
-	0	430/101.420.ccis. and (amsotropic with conductor)	EPO; JPO;	2002/07/09
	= 0000	stadina su ob su o disconsissad	DERWENT	15:37
-	73888	stacking or 3D or 3-dimensional	EPO; JPO;	2002/07/09
į	_	-0/6 1 1(+1) D 11 + 1	DERWENT	15:38
-	0	438/for.426.ccls. and (stacking or 3D or 3-dimensional)	EPO; JPO;	2002/07/09
			DERWENT	15:37
-	9	(anisotropic with conductor) and (stacking or 3D or	EPO; JPO;	2002/07/09
		3-dimensional)	DERWENT	15:40
-	1501	438/for.369.ccls.	EPO; JPO;	2002/07/09
			DERWENT	15:40
-	0	(anisotropic with conductor) and 438/for.369.ccls.	EPO; JPO;	2002/07/09
			DERWENT	15:40
-	5	(stacking or 3D or 3-dimensional) and 438/for.369.ccls.	EPO; JPO;	2002/07/09
	3	1	DERWENT	15:40
_	179315	"light emitting diodes" or LED	USPAT;	2002/07/10
	*/30±0	ng ctting diodes of DDD	US-PGPUB	15:56
_ 1	587	anisotropic with conductor	USPAT;	2002/07/10
	20/	amouropic with conductor		
	~ 0	("light emitting diodes" on LED) and (animates with	US-PGPUB	15:56
-	38			
	38	("light emitting diodes" or LED) and (anisotropic with conductor)	USPAT; US-PGPUB	2002/07 15:56